

FIG. 1A

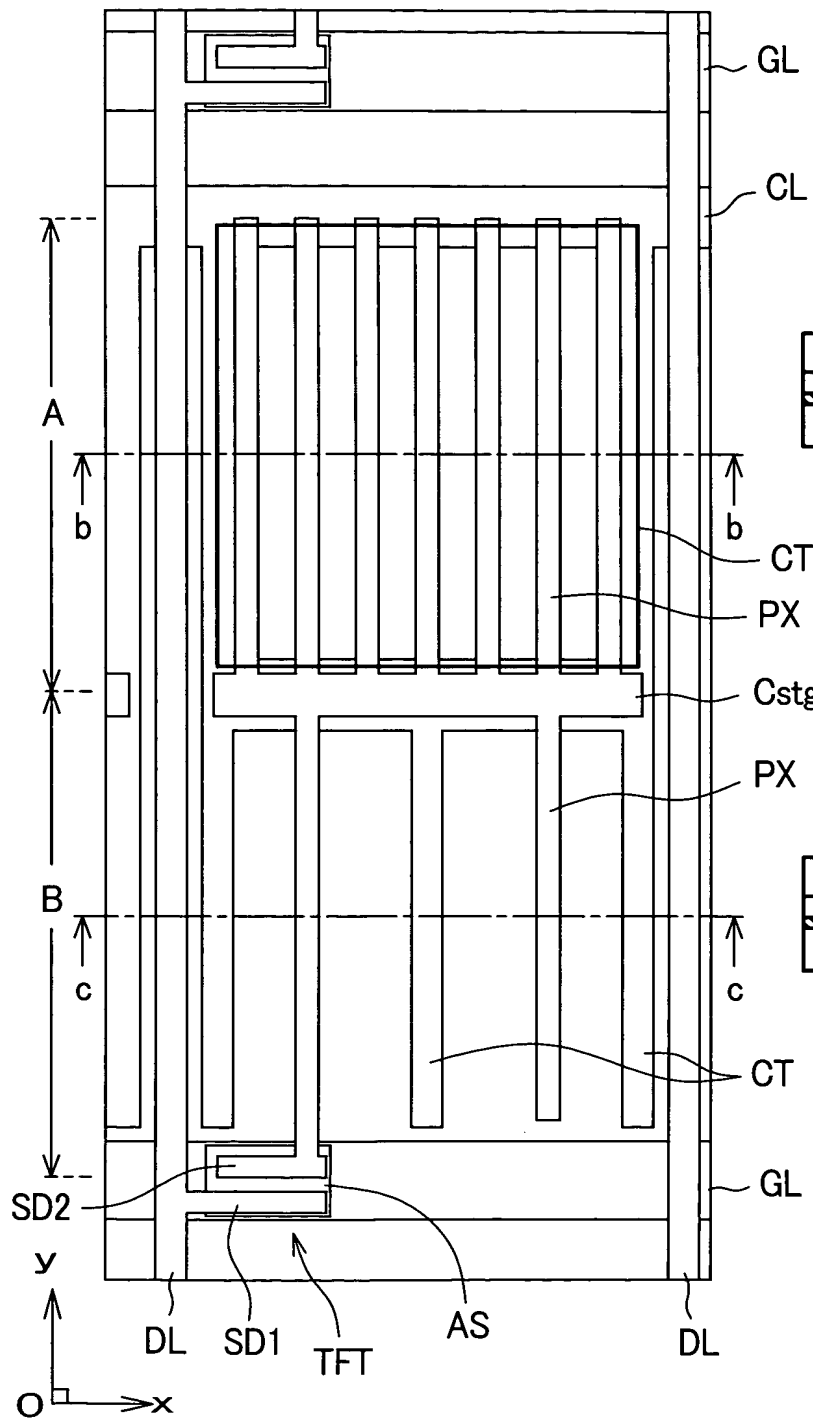


FIG. 1B

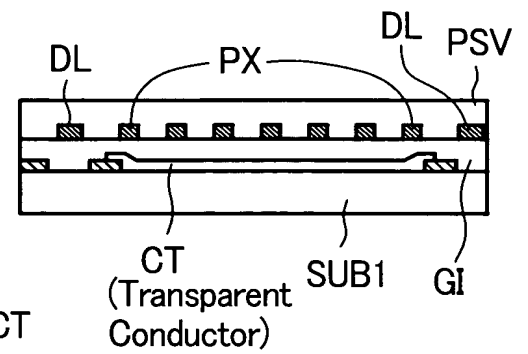


FIG. 1C

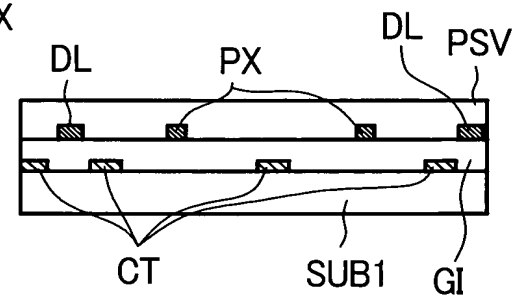


FIG. 2

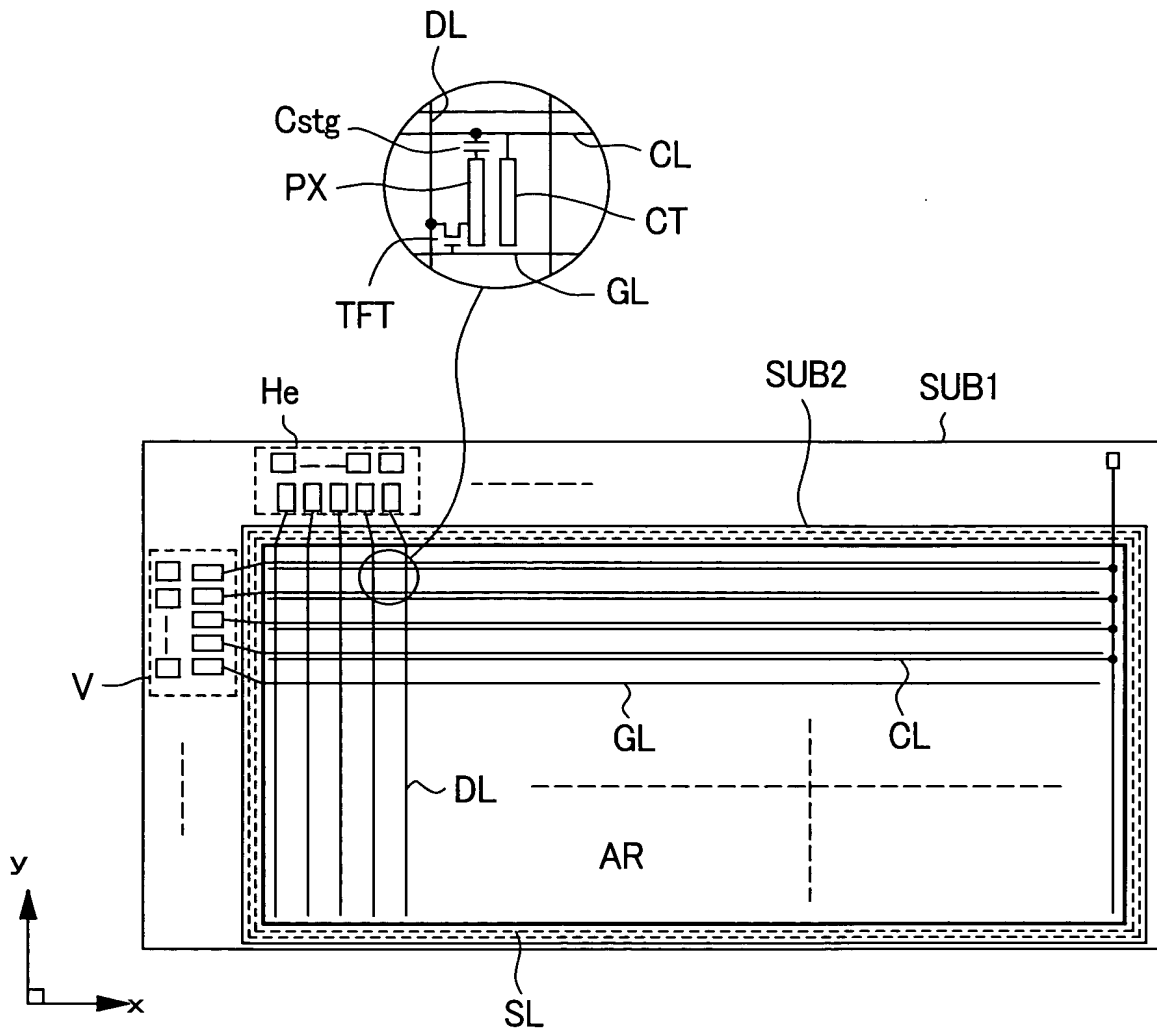


FIG. 3A

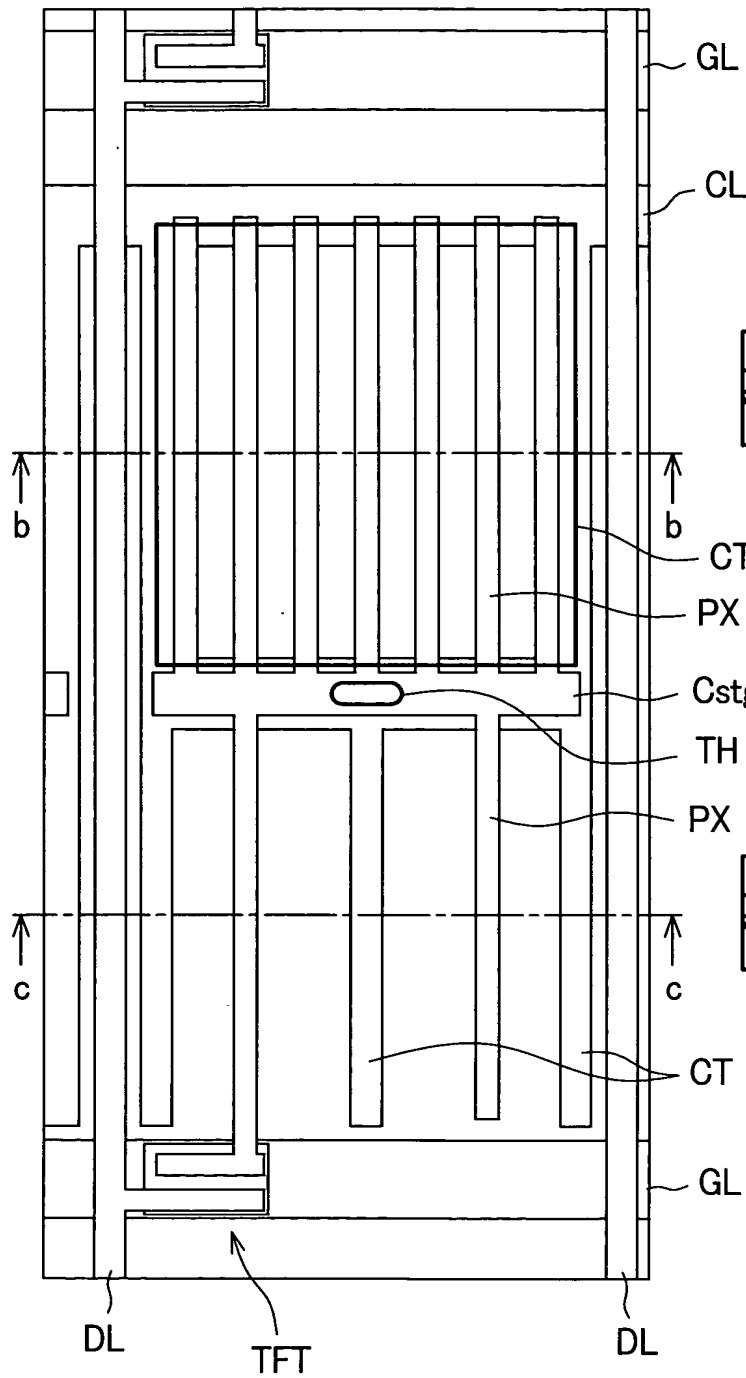


FIG. 3B

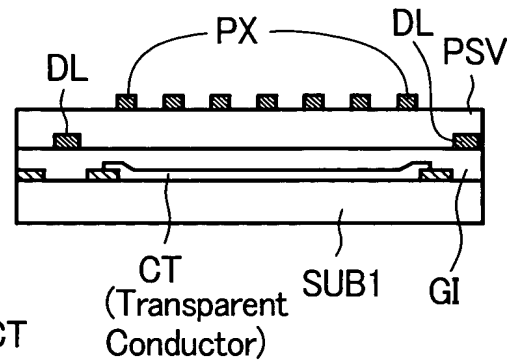


FIG. 3C

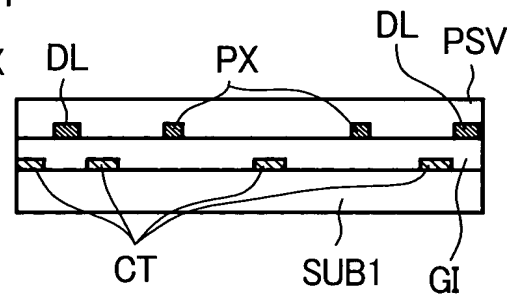


FIG. 4A

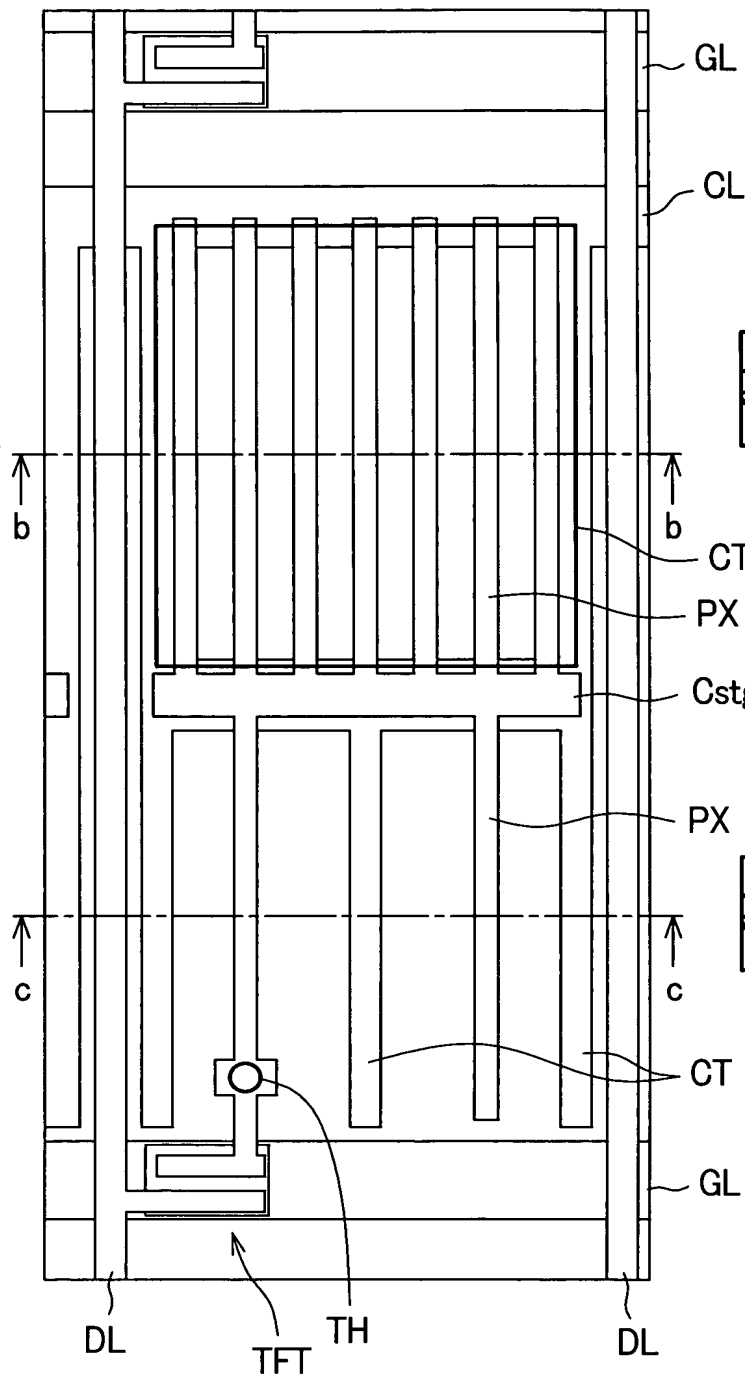


FIG. 4B

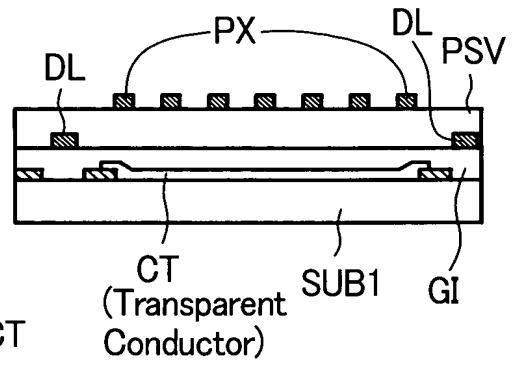


FIG. 4C

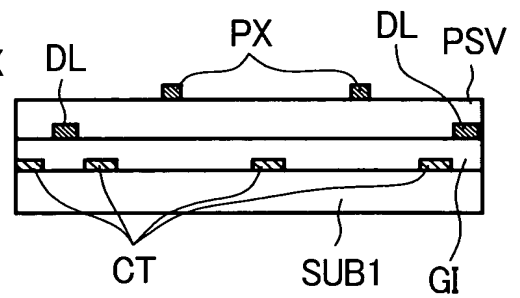


FIG. 6A

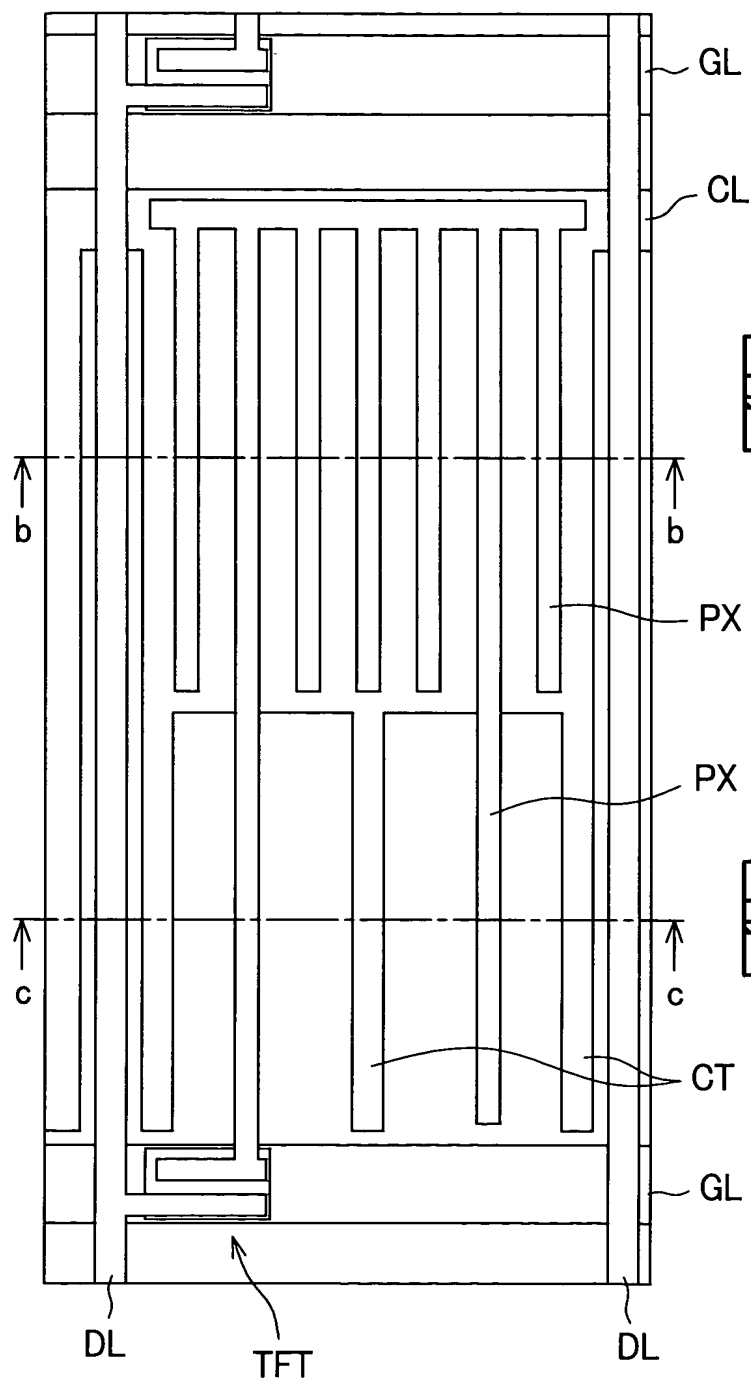


FIG. 6B

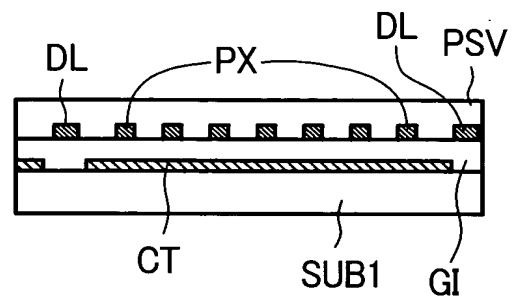


FIG. 6C

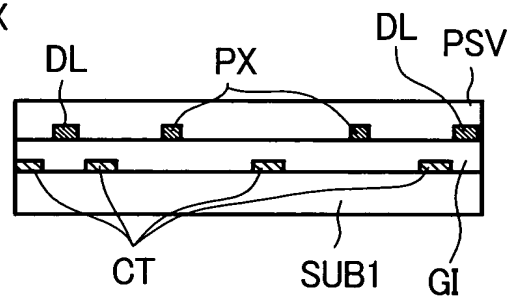


FIG. 7A

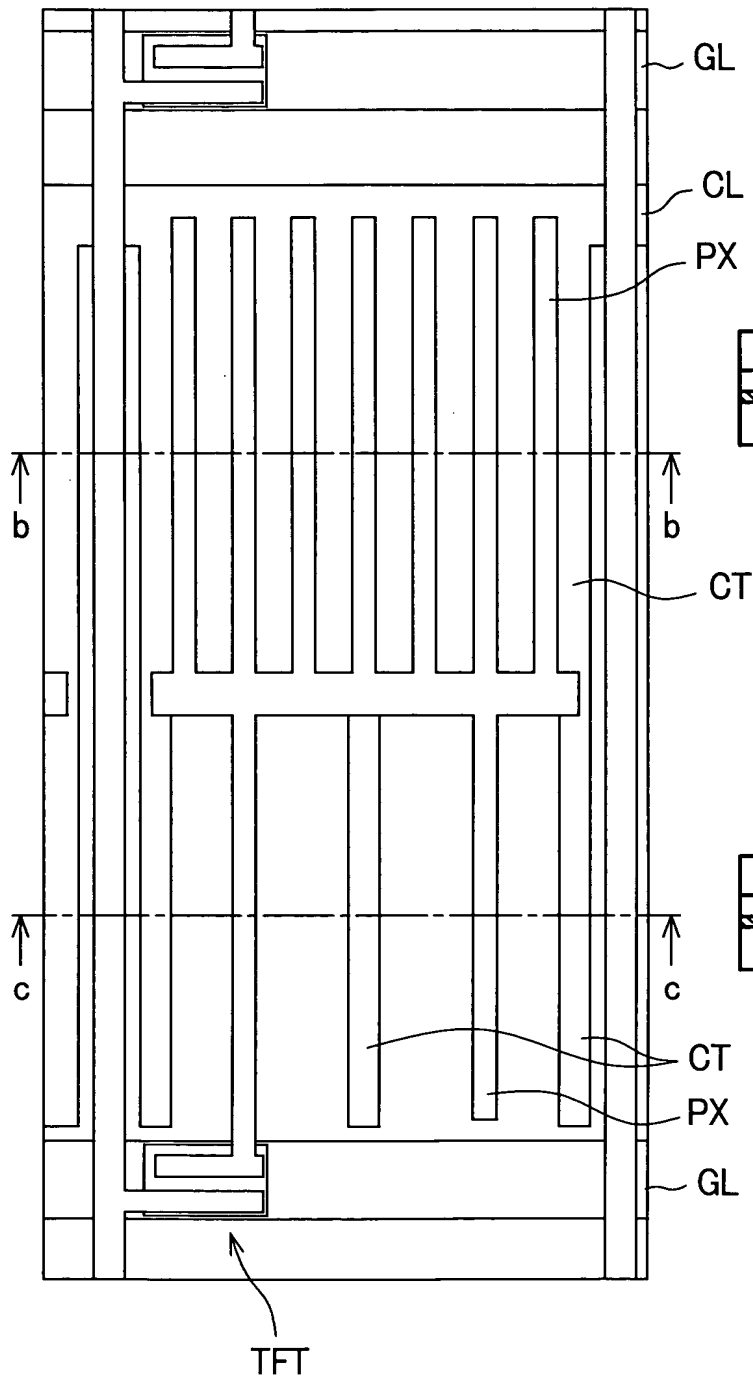


FIG. 7B

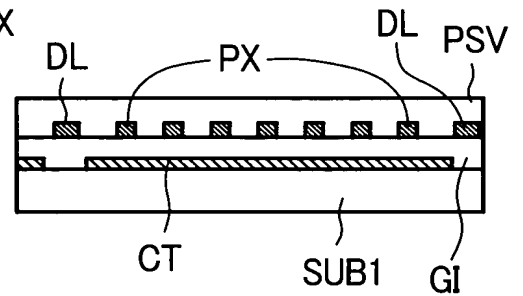


FIG. 7C

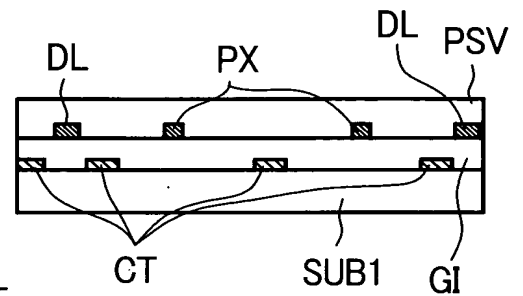


FIG. 8

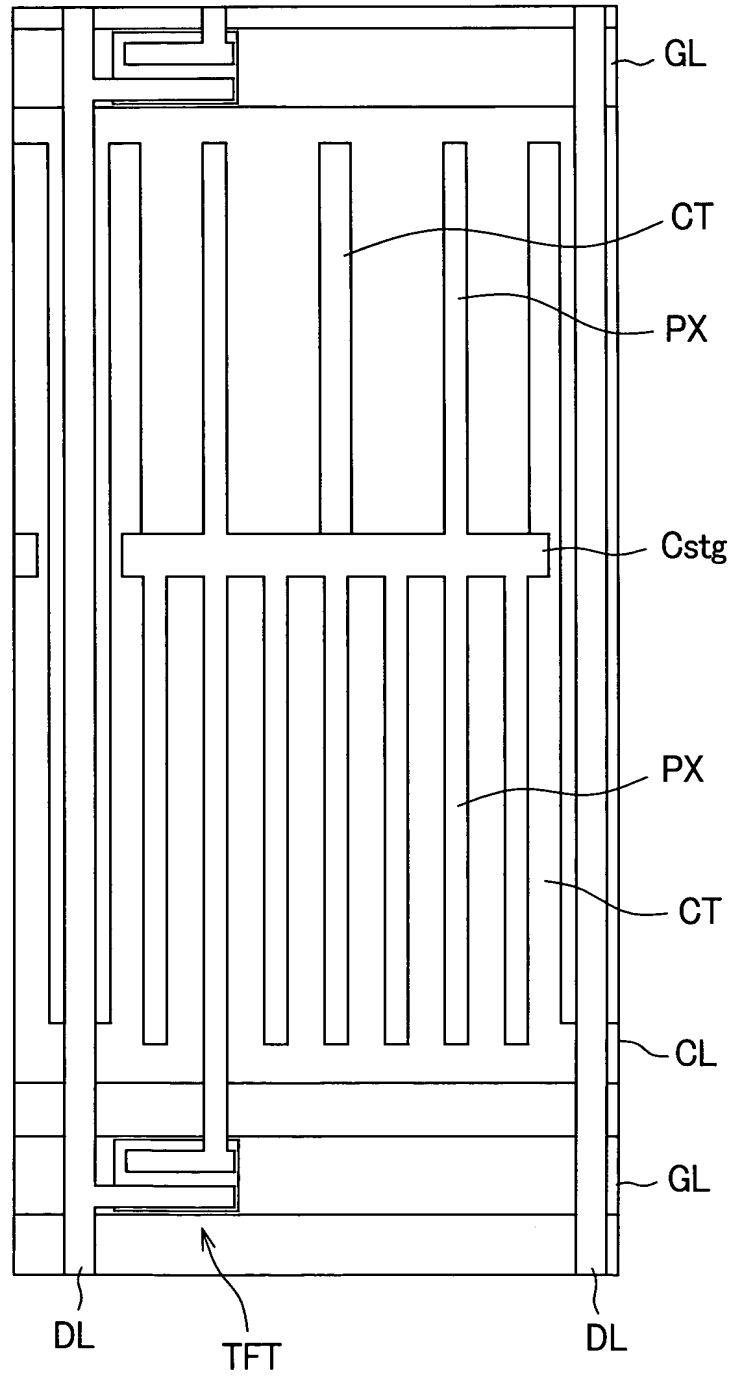


FIG. 9A

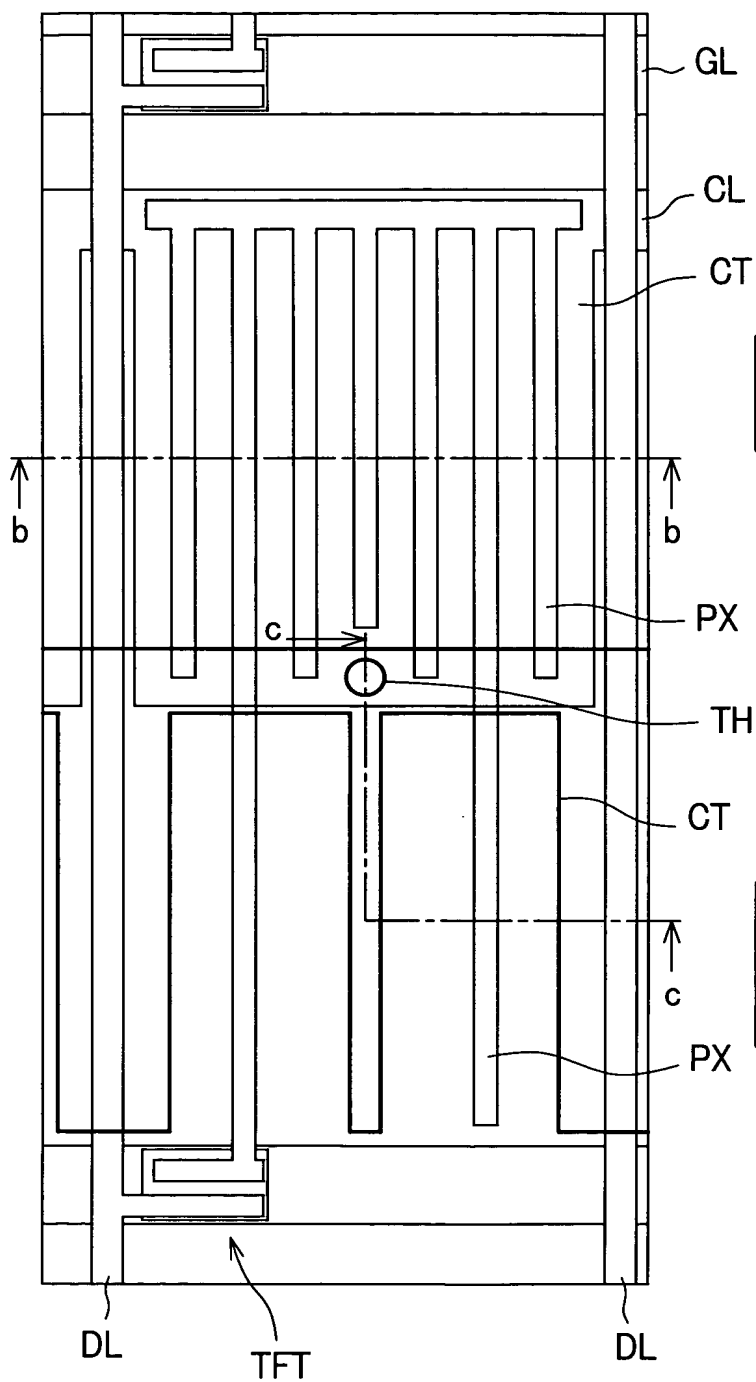


FIG. 9B

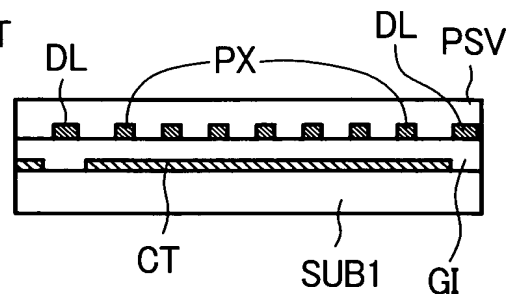


FIG. 9C

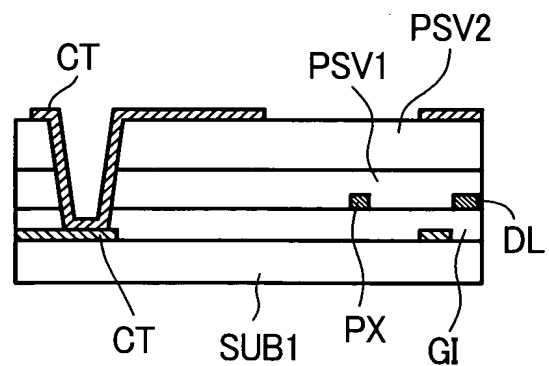


FIG. 10A

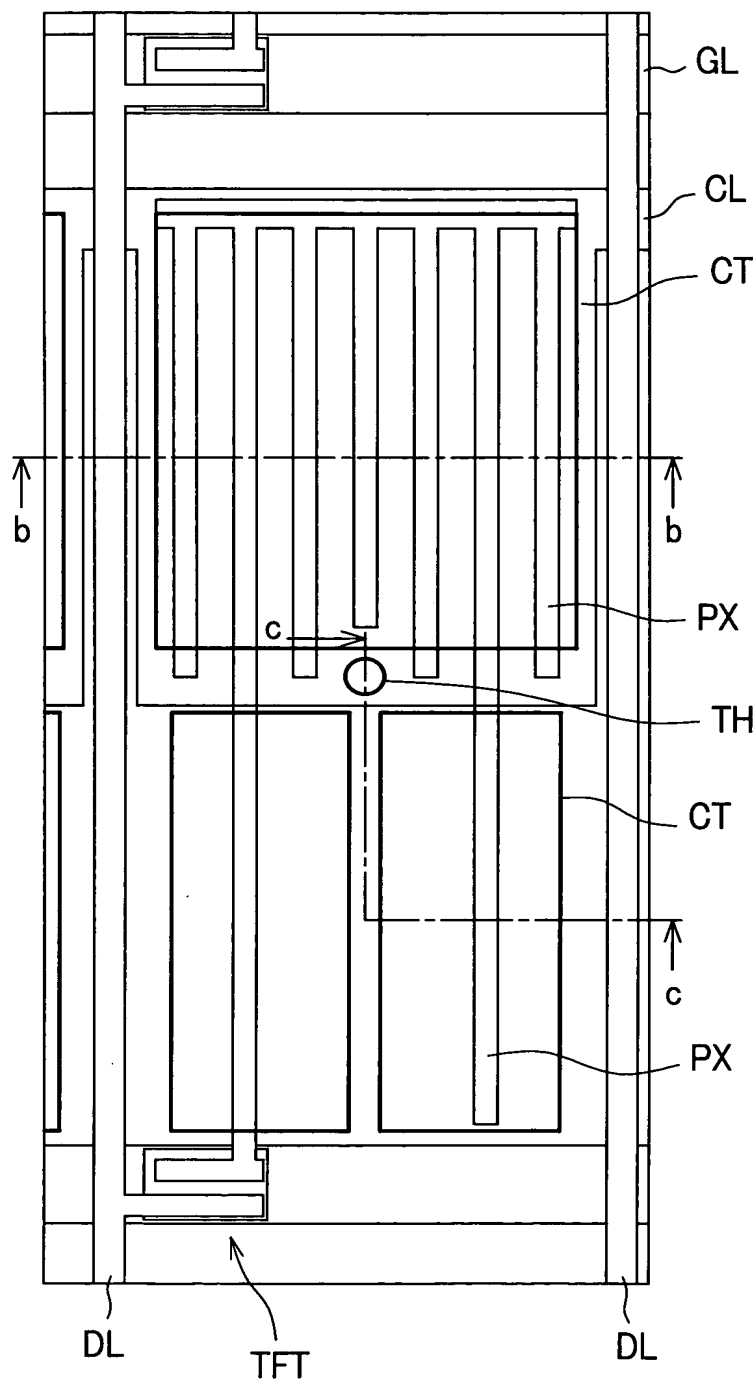


FIG. 10B

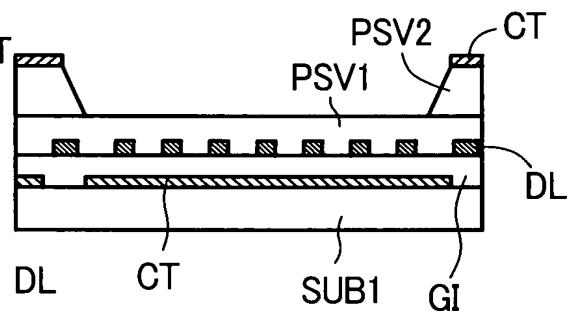


FIG. 10C

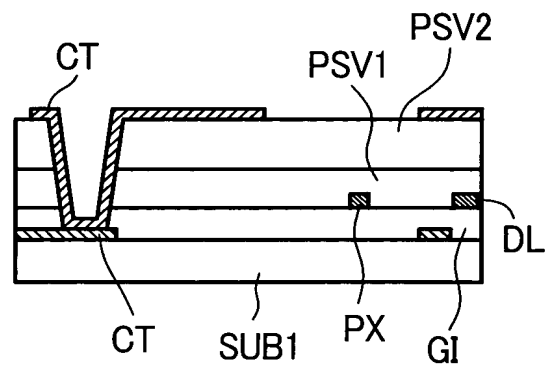


FIG. 11

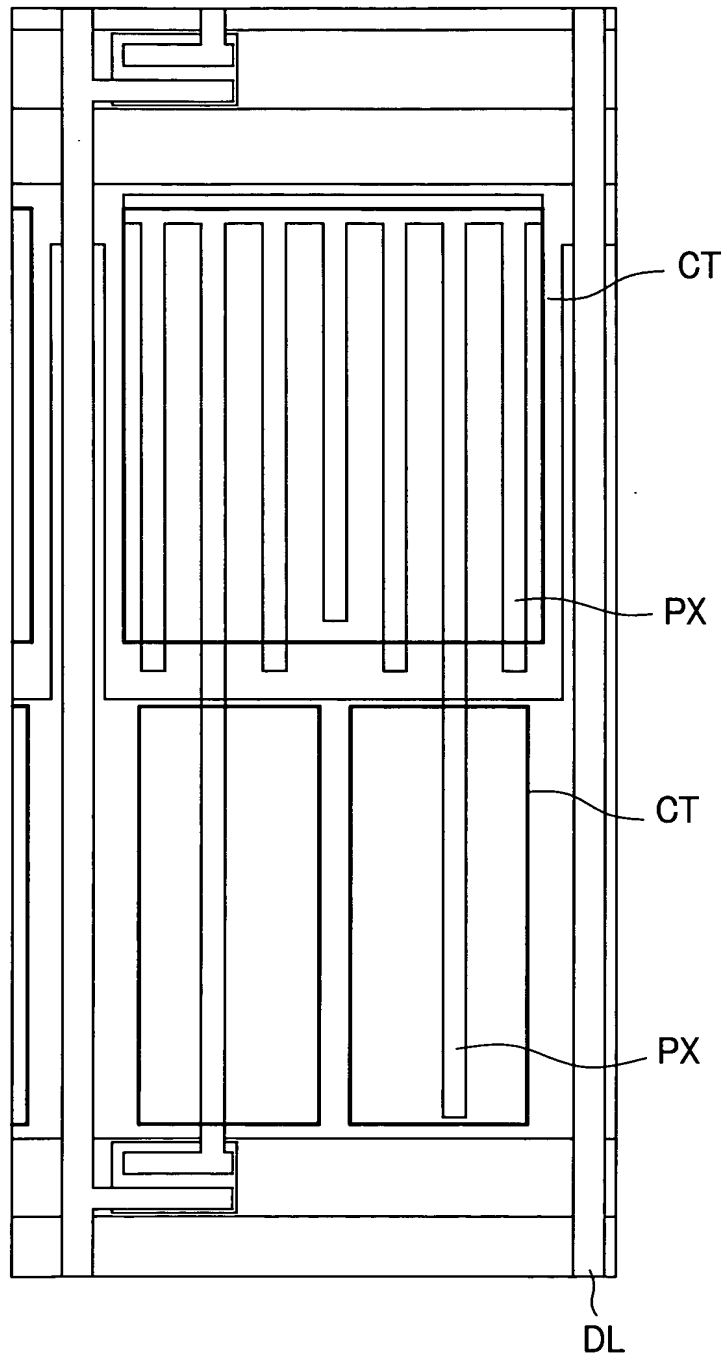


FIG. 12

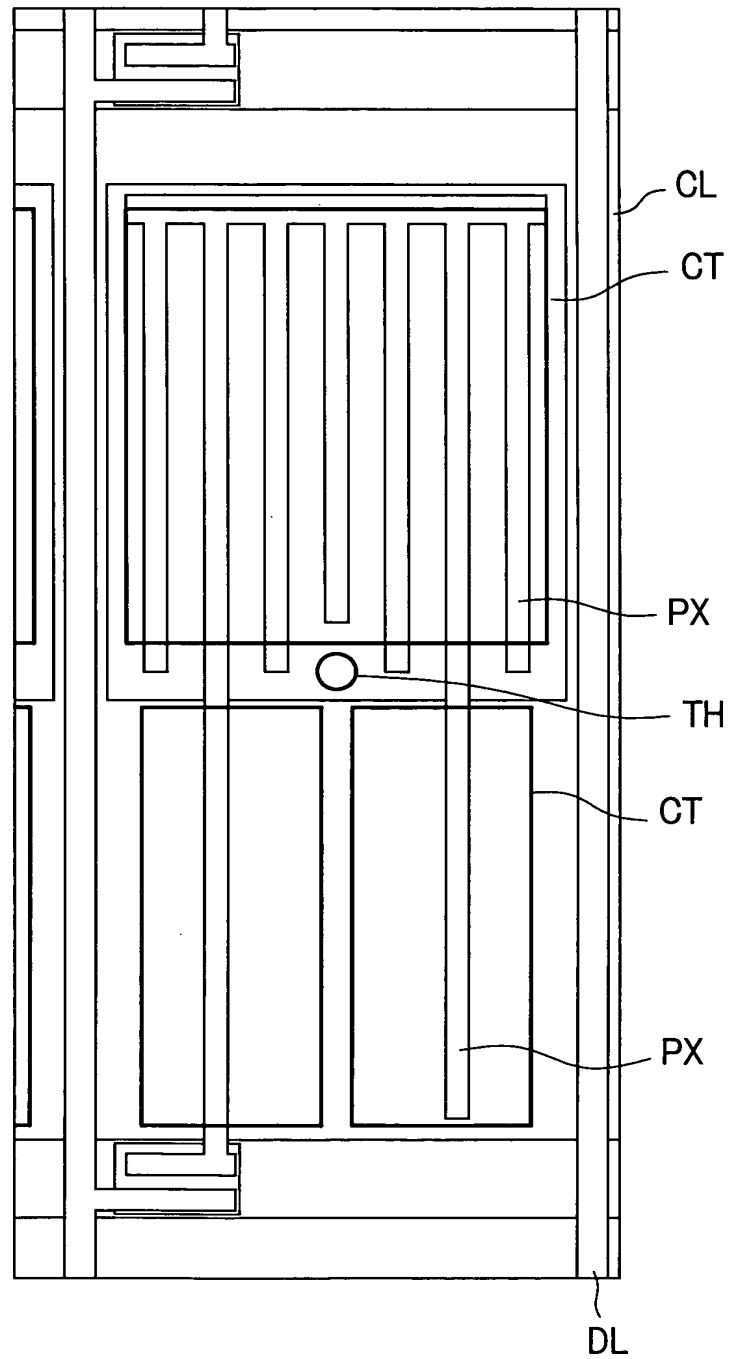


FIG. 13A

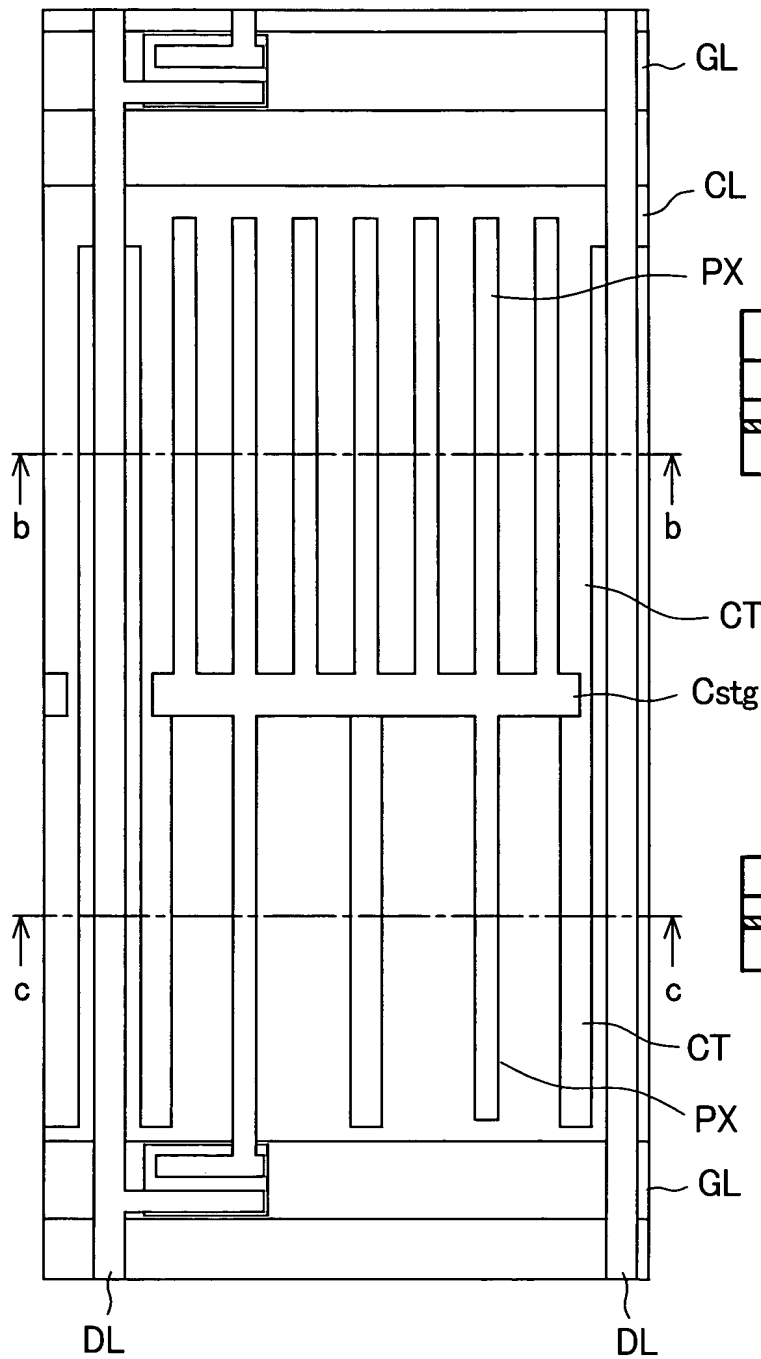


FIG. 13B

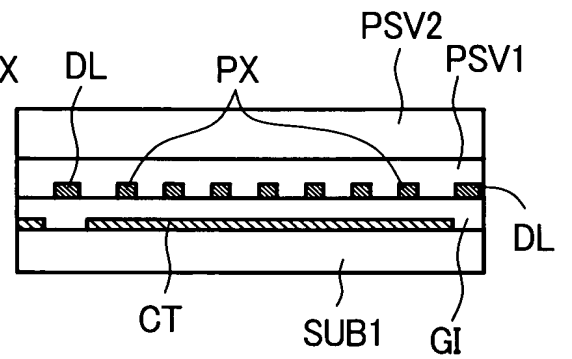


FIG. 13C

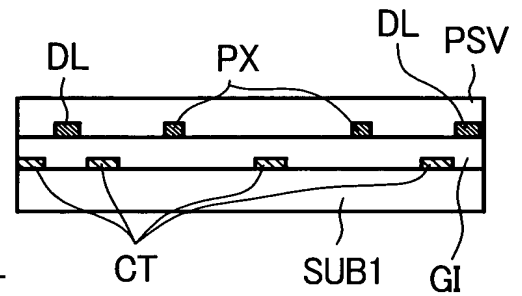


Diagram illustrating a cross-sectional view of a semiconductor device structure. The structure features a central array of vertical elements (PX) flanked by horizontal elements (GL, CL). The central array is connected to a common bus (Cstg) and individual gates (CT). Dimensions b and c are indicated, representing the height of the central array and the width of the individual gates, respectively. The structure is labeled with DL at the bottom corners.

[illegible]

FIG. 15A

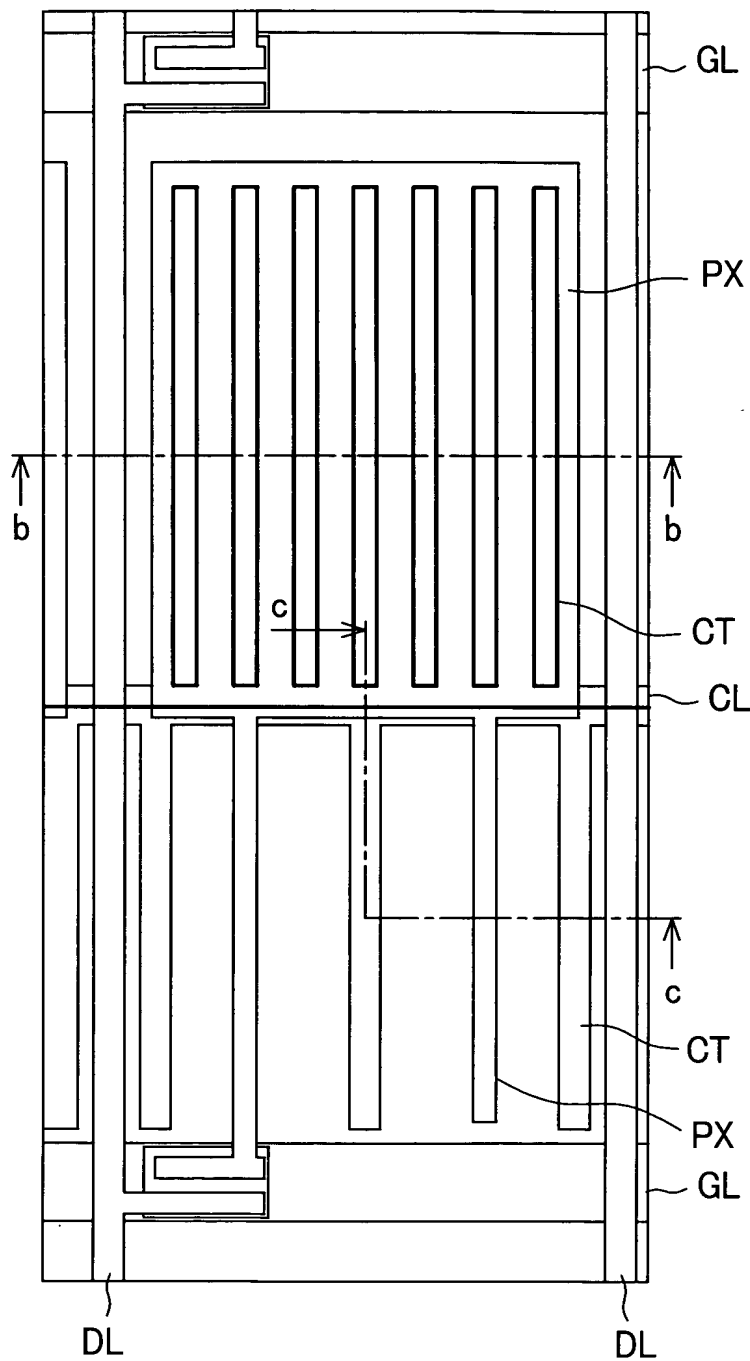


FIG. 15B

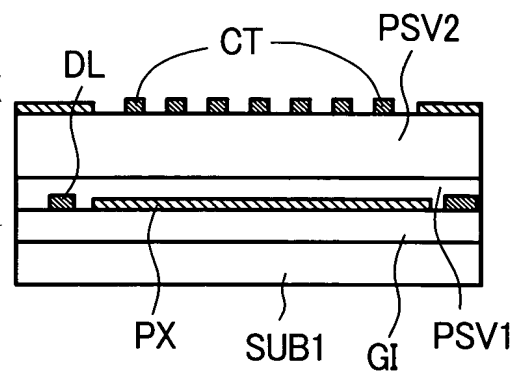


FIG. 15C

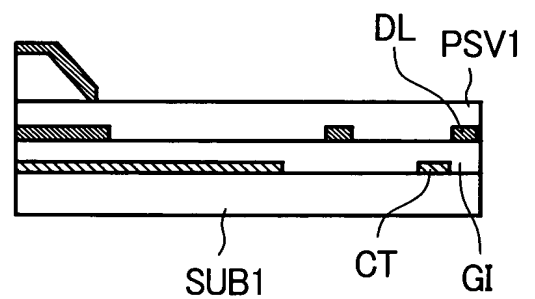


FIG. 16A

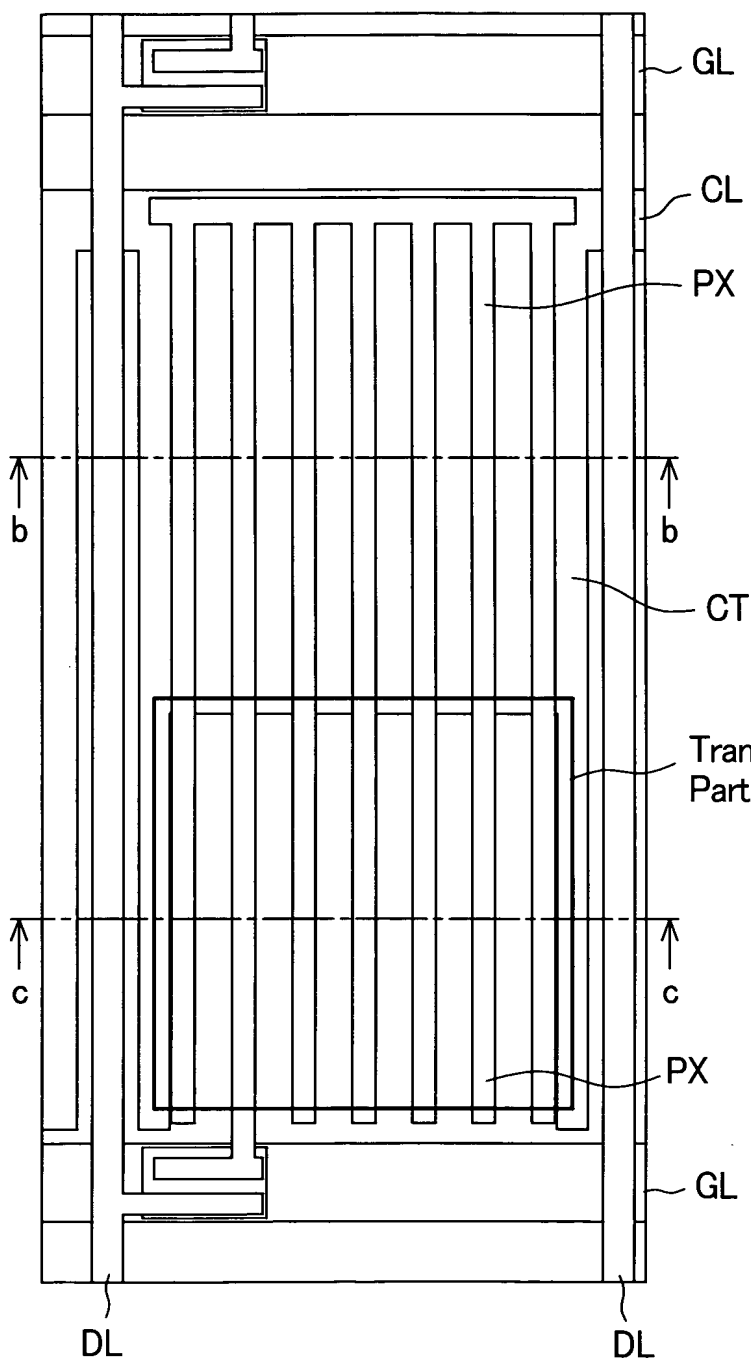


FIG. 16B

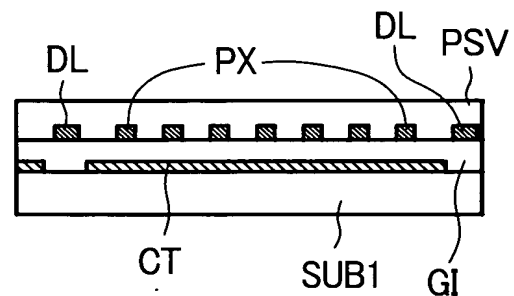


FIG. 16C

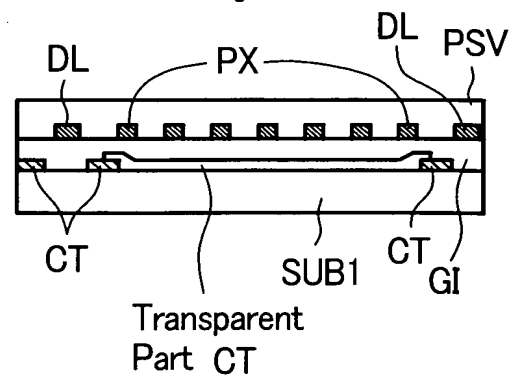


FIG. 17A

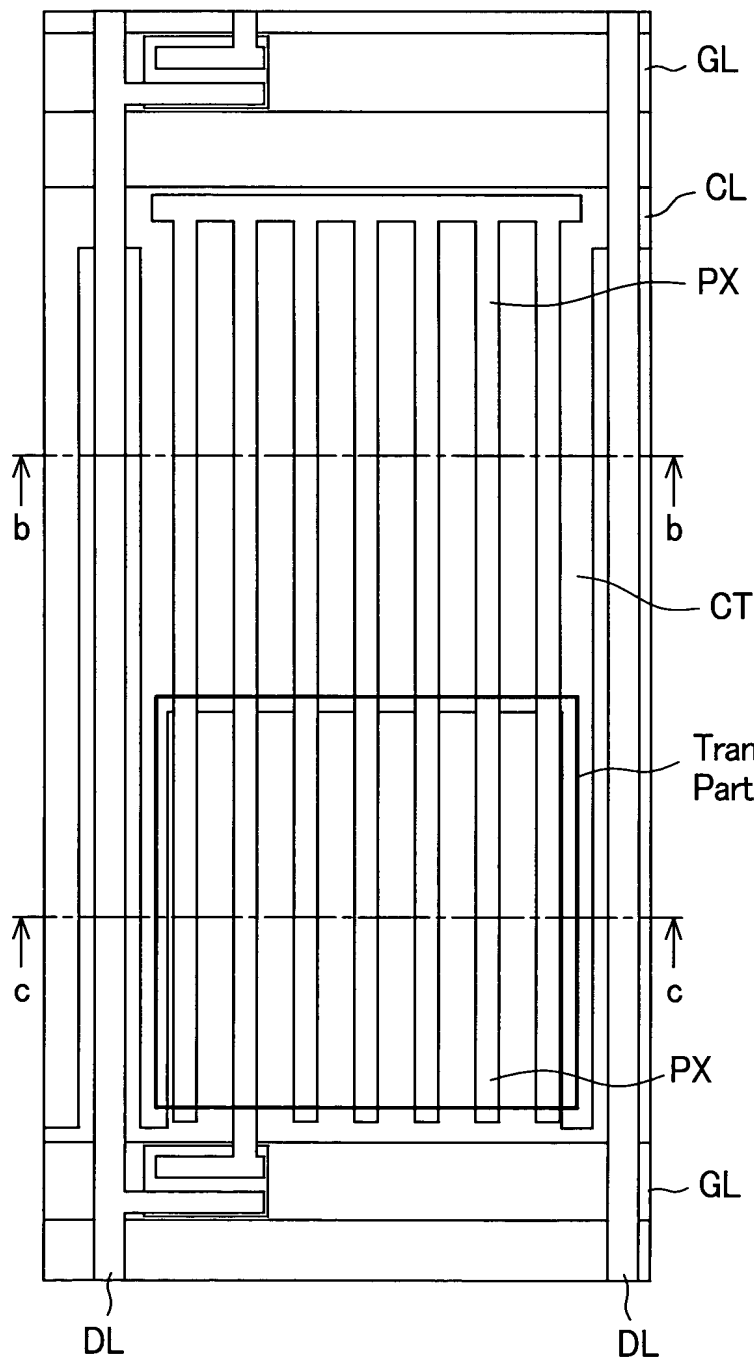


FIG. 17B

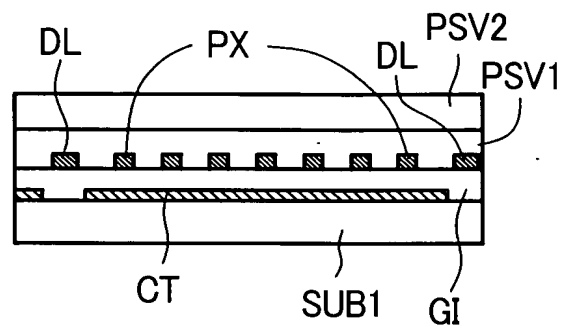
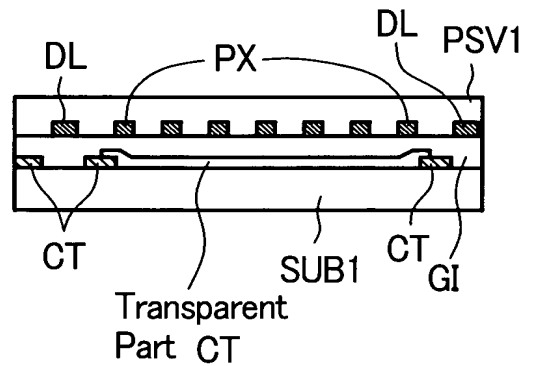


FIG. 17C



This diagram shows a cross-sectional view of a transparent part CT in a second embodiment. The structure consists of a substrate SUB1 with a thin layer GI on its top surface. A transparent part CT is formed on the GI layer, featuring a central transparent region and side regions with a hatched pattern. Labels include DL for the hatched regions, PX for the transparent part, and PSV for a protective layer on the top surface of the substrate.

FIG. 19A

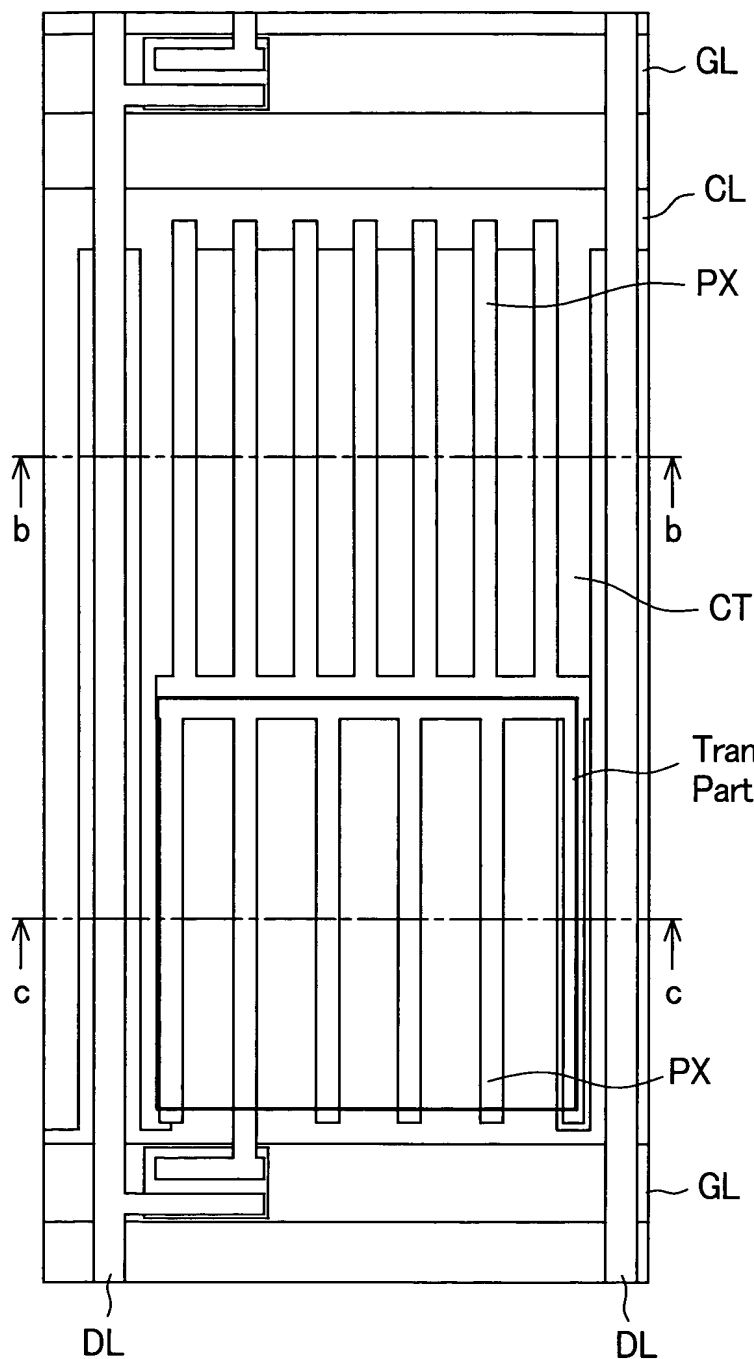


FIG. 19B

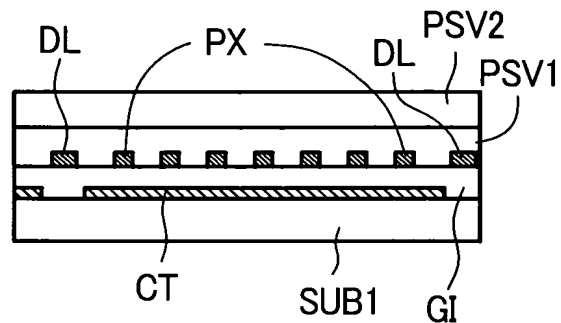


FIG. 19C

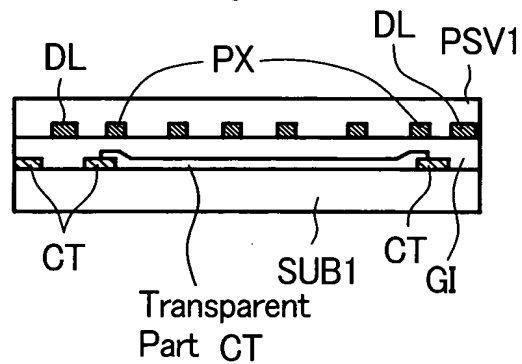


FIG. 20A

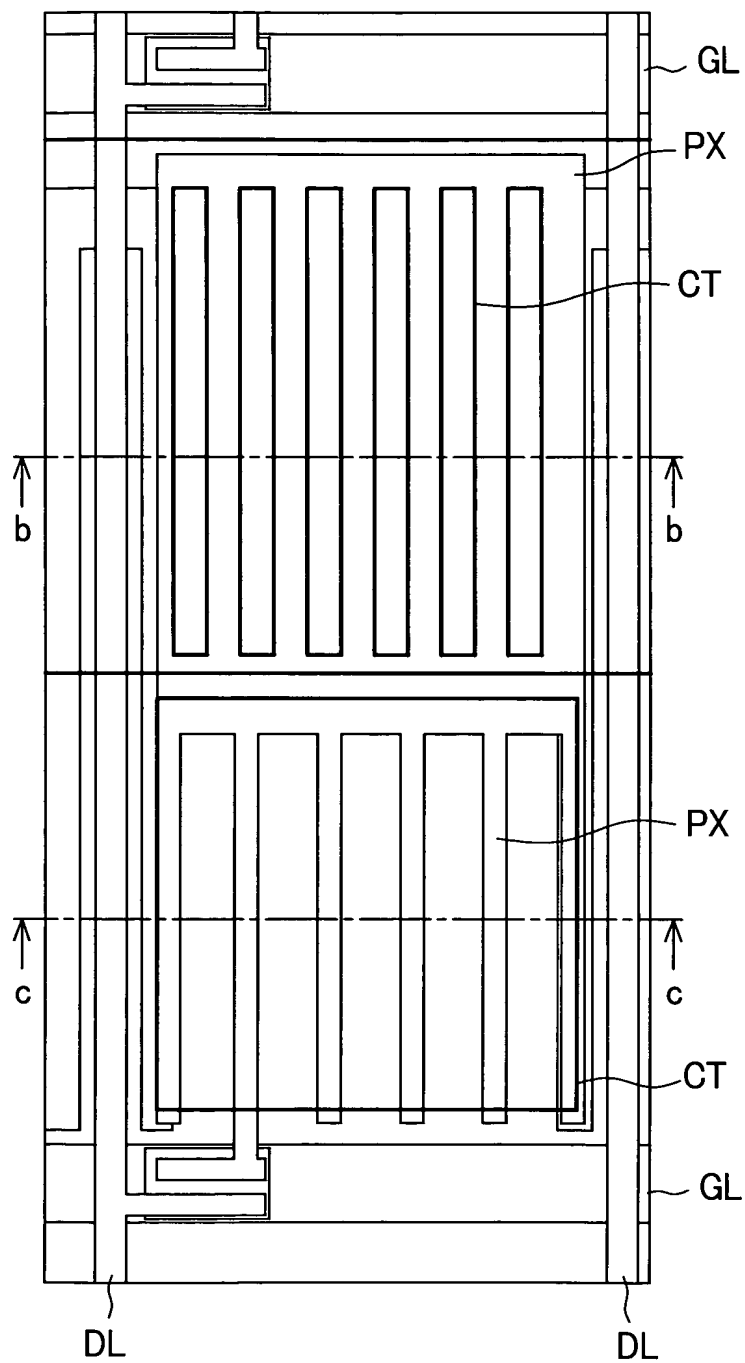


FIG. 20B

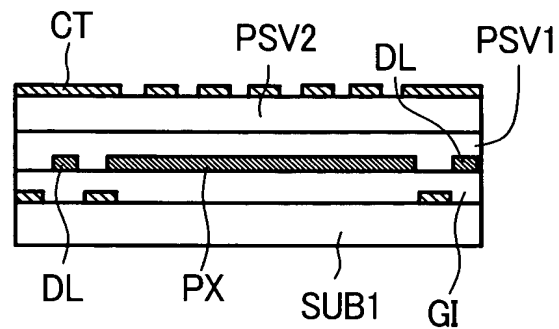


FIG. 20C

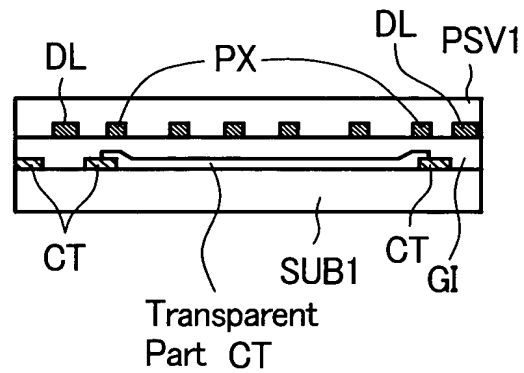


FIG. 21A

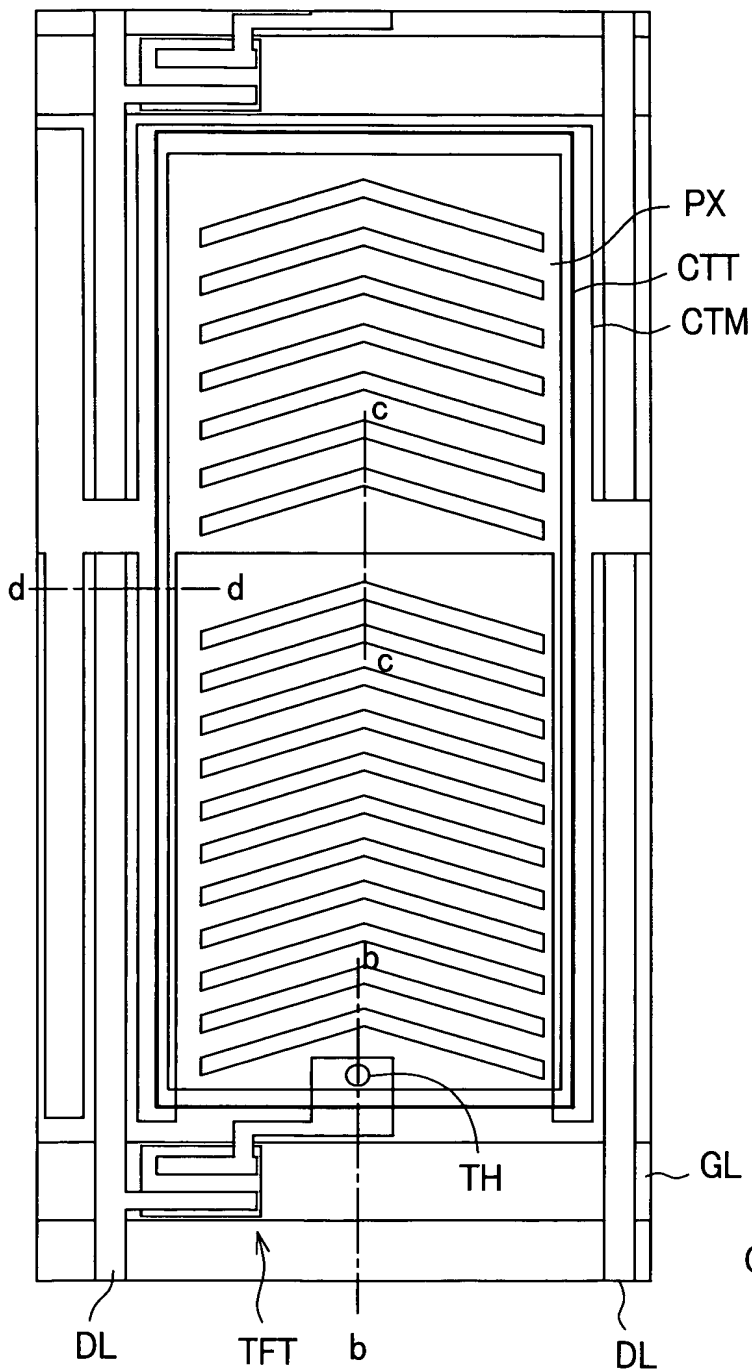


FIG. 21B

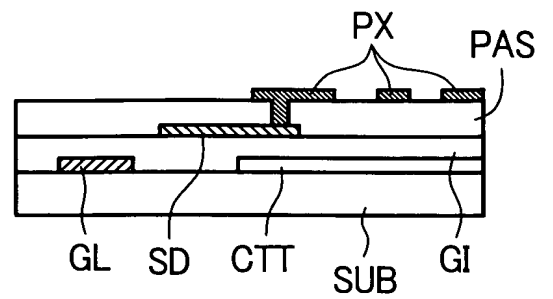


FIG. 21C

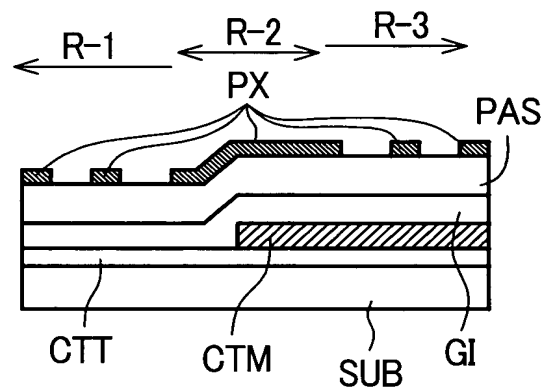


FIG. 21D

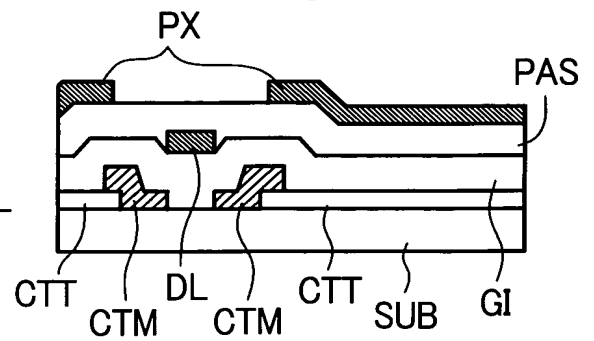


FIG. 22

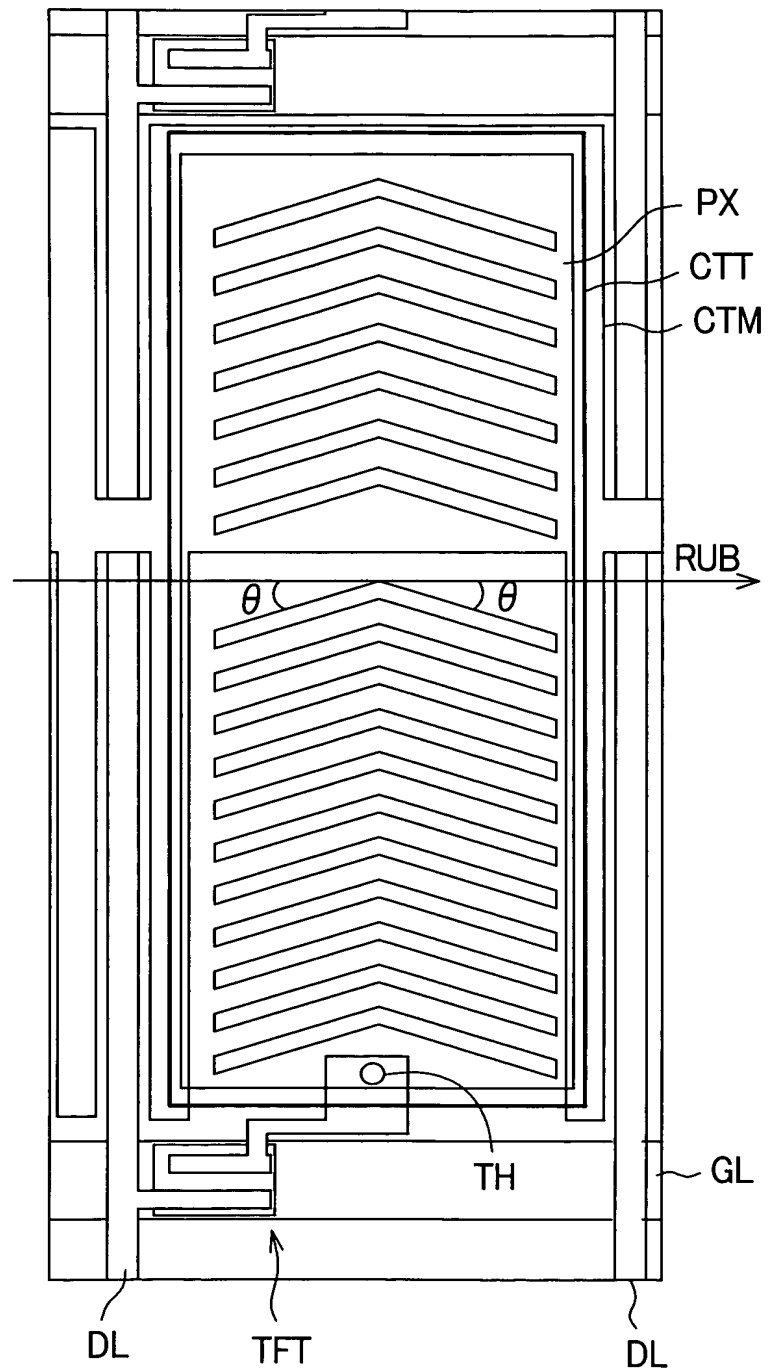


FIG. 23A

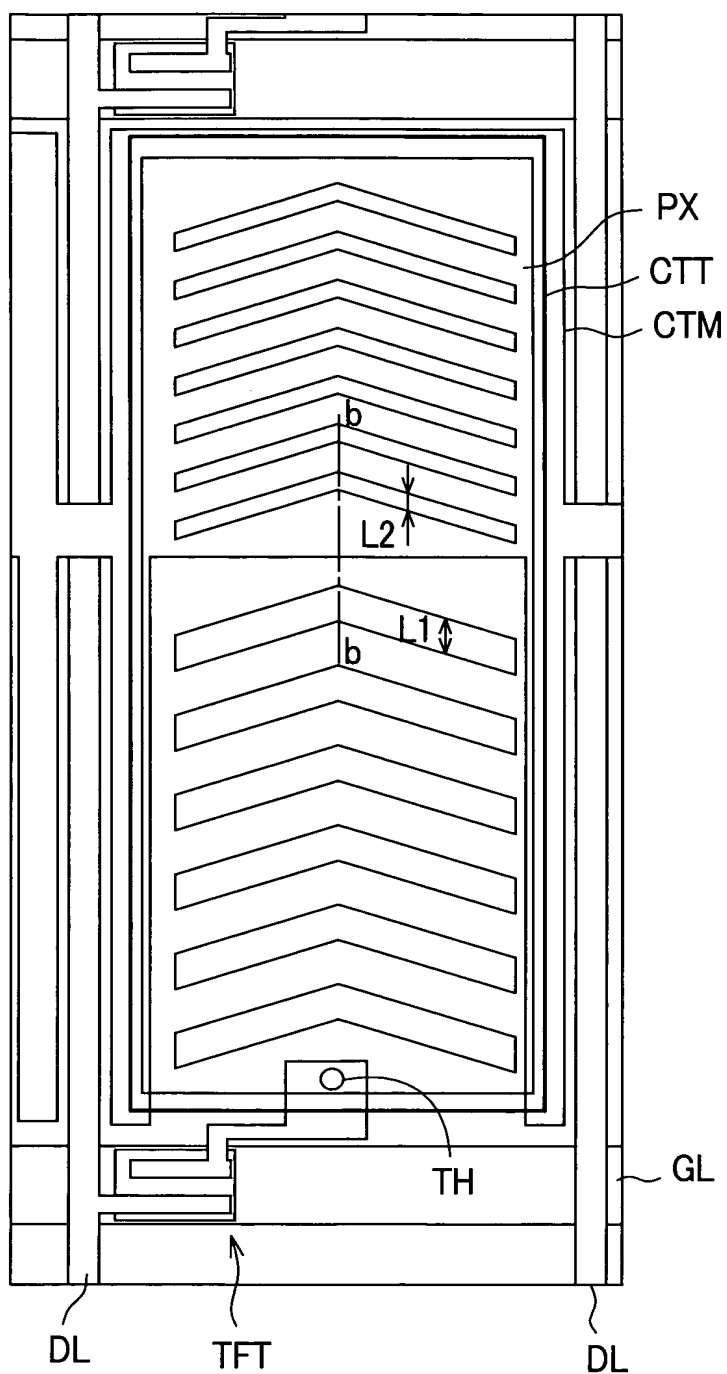


FIG. 23B

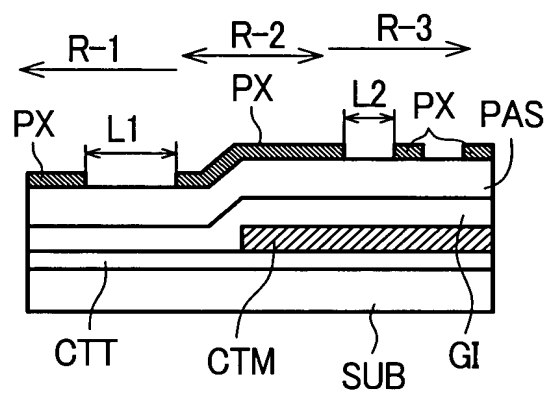


FIG. 24A

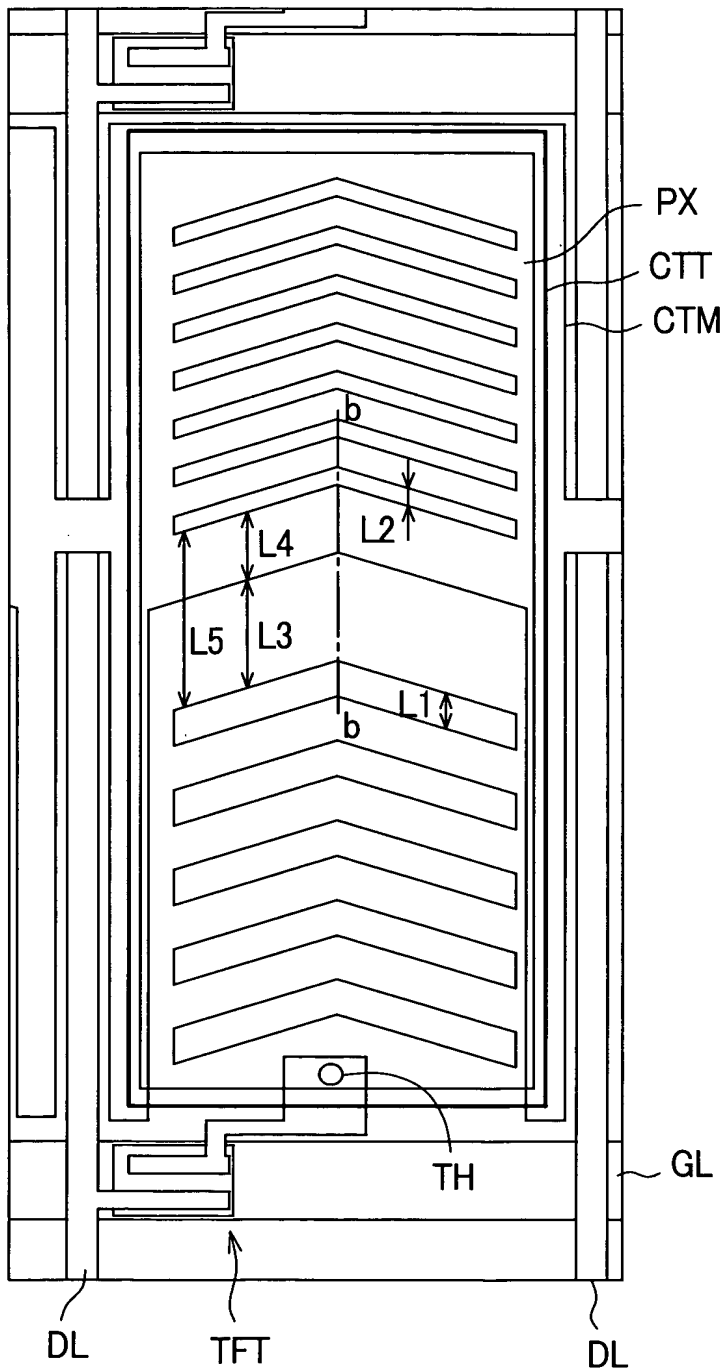


FIG. 24B

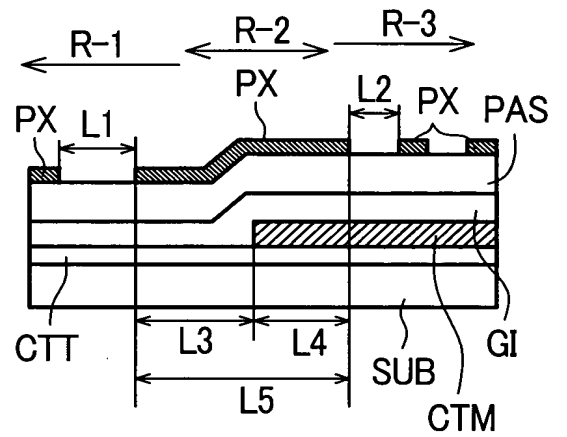


FIG. 25A

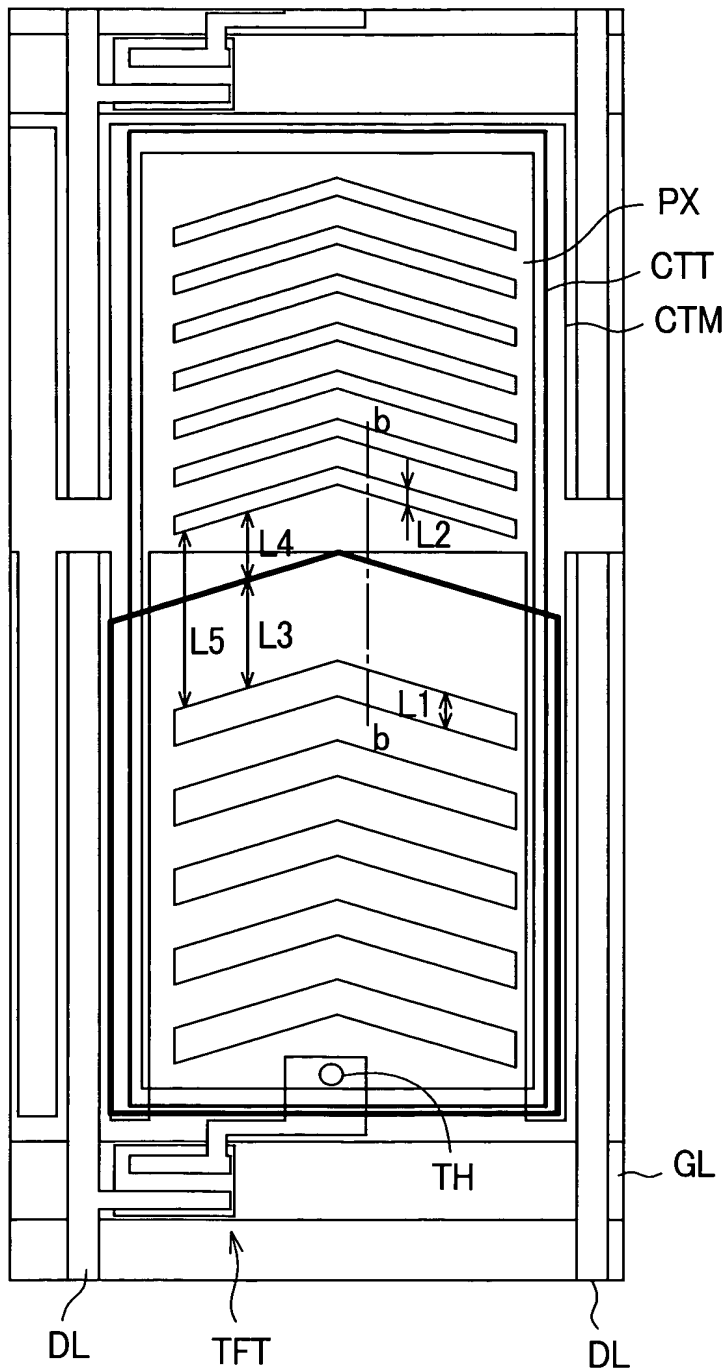


FIG. 25B

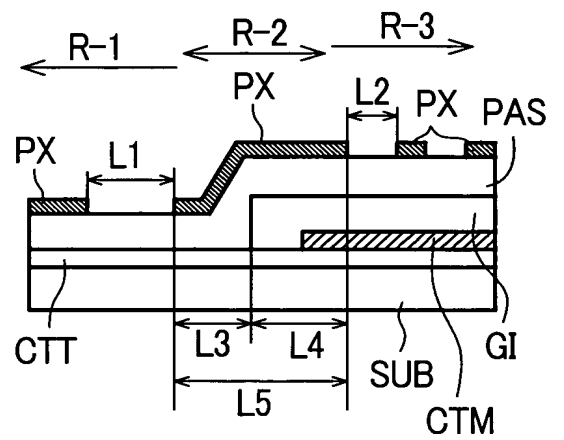


FIG. 26A

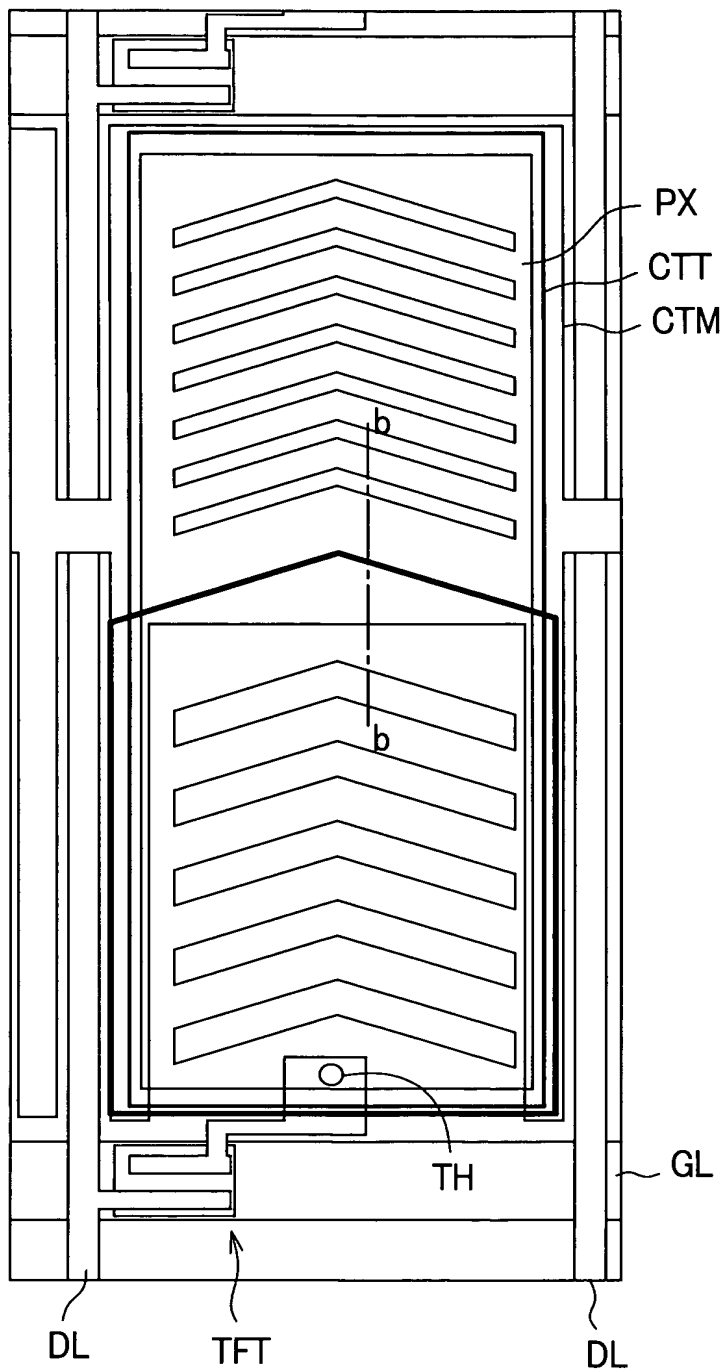


FIG. 26B

